

BHT 11/6/09

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	33	("20010000684" "20040250989" "20050201063" "20050276021" "20060056154" "20060067054" "20060139891" "20070195489" "20070263361" "4825337" "5680295" "5161087" "5966287" "6130820" "6278610" "6297966" "6707673" "6775139" "6775139" "6888719" "6119765" "6188576" "6233150" "6266244" "6424532" "6466441" "6535387" "7187552" "7312996" "7349219" "7349220" "7365985" "7400506" "7403383").PN.	US_PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 10:49
S2	1470	361/697.ccls.	US_PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 10:54
S3	1	(11/308849).APP.	USPAT; USOCR	AND	ON	2009/11/06 11:10
S4	14	("20030026076" "20050276021" "5109318" "5815371" "5966287" "6119765" "6233150" "6535387" "7079396" "7221569").PN. OR ("7349220").URPN.	US_PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:10
S5	1	(11/309225).APP.	USPAT; USOCR	AND	ON	2009/11/06 11:18

S6	16	("20030026076" "20070195489" "20070206359" "20070217160" "3506877" "5109318" "5966287" "6119765" "6233150" "6297966" "6765797" "7221569" "7349220" "7375964").PN. OR ("7457122").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:19
S7	8	("20060056160" "6535387" "6775139" "7023700" "7079396").PN. OR ("7312996").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:25
S8	18	("5335722" "5835347" "5973921" "6269001" "6435889").PN. OR ("6775139").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:26
S9	29	("4858070" "4916575" "5594204" "5596486" "5608610" "5657203" "5671122" "5703754" "5731633").PN. OR ("6188576").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:39
S10	20	("3877064" "4298237" "4314311" "4761140" "4806103" "4978638" "5109980" "5161087" "5174780" "5648890" "5757622" "5761036" "5769668" "6188576" "6246583" "6308772" "6362966").PN. OR ("6890202").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:47
S11	7	("5161087" "5764482" "6088228" "6188576").PN. OR ("6278610").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:51

S12	18	("20070139897" "3893161" "4872505" "5109318" "5513135" "5784263" "5901781" "6173382" "6208517" "6278610" "6424532" "6507912" "6510100" "6535387" "7079396" "7106595" "7312996" "7403383"). PN. OR ("7474529"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:54
S13	22	("20010019913" "20060221573" "20070070607" "20070217160" "3582865" "5966289" "6201699" "6310776" "6330160" "6407923" "6411511" "6434005" "6535387" "6765797" "6775139" "6934152" "7023700" "7023701" "7106595" "7142428" "7151669" "7365990"). PN. OR ("7480147"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:55
S14	29	("20050201063" "5581442" "5761041" "5966287" "6025992" "6031727" "6119765" "6130820" "6201695" "6319756" "6424532" "6449156" "6449161" "6765797" "6775139" "6888719" "6969907"). PN. OR ("7079396"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 11:59
S15	1470	361/697.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 12:15

S16	0	S15 (((memory or DIMM or SIMM or multichip) near2 (board or stick or module orDIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:15
S17	1	S15 (((memory or DIMM or SIMM or multichip) near2 (board or stick or module orDIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:16
S18	0	S15 (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:17
S19	1	361/704.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:18

S20	0	361/715-716.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:18
S21	2	361/719.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:19
S22	0	361/728.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:34
S23	0	361/729.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:34

S24	0	361/717.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:34
S25	0	361/748.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:34
S26	2	165/80.3.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:35
S27	1	165/80.2.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:35

S28	0	165/104.33.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:36
S29	2	165/185.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:36
S30	0	174/16.3.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:36
S31	0	257/675.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:37

S32	1	257/706.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:37
S33	0	257/720.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:38
S34	0	257/727.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:38
S35	0	62/259.2.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:38

S36	0	439/485.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:39
S37	0	439/487.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:39
S38	1	"439"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:40
S39	0	"62"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:41

S40	2	"174"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:41
S41	1	"439"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:42
S42	6	"257"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:42
S43	7	"165"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:43

S44	0	361/709.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:43
S45	1	361/711.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:44
S46	0	361/707.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:44
S47	9	"361"/\$.ccls. (((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SI MM)) ((heatsink or (heat near2 sink) or ((cool or cold) near2 plate)) with (pivot\$4 or rotat\$4 or tilt)) axis (grid or fin))	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:44

S48	6	"361"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) (heat or thermal\$3) (cool or cold) (grid or fin) (PCB or board or substrate or carrier) (plug near2 in) slot)	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:46
S49	0	"165"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (board or stick or module or DIMM or SIMM)) (heat or thermal\$3) (cool or cold) (grid or fin) (PCB or board or substrate or carrier) (plug near2 in) slot)	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:47
S50	0	"257"/\$.ccls. (((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (heat or thermal \$3) (cool or cold) (grid or fin) (PCB or board or substrate or carrier) (plug near2 in) slot)	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:47
S51	33	(((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (heat or thermal\$3) (cool or cold) (grid or fin) (PCB or board or substrate or carrier) (plug near2 in) slot)	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:47
S52	27	S51 not S48	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:47

S53	8312	((memory or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (PCB or board or substrate or carrier) (plug or insert) slot)	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 12:48
S54	277	S53 (heatsink or ((heat or thermal or cool or cold or radiat\$3) near3 (plate or sink)) (rotat\$4 or pivot or tilt) axis)	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 12:50
S55	277	S54 (PCB or board or substrate or carrier)	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 12:50
S56	182	S54 ((cool or heat or radiat\$3 or dissipat\$3) with (IC or chip or semiconductor or memory))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 12:51
S57	4	S56 ((heatsink or ((heat or thermal or cool or cold or radiat\$3) near3 (plate or sink)) with (rotat\$4 or pivot or tilt))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:12
S58	969	"361"/\$.ccls. ((memory near2 module) cool\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 13:14
S59	595	S58 ((heatsink or (heat or thermal or cool or cold or radiat\$3)) near3 (spread\$3 or plate or sink))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:15
S60	49	S59 ((spread\$3 or plate or sink) with (rotat\$4 or pivot or tilt))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:16
S61	1638	"361"/\$.ccls. (((SIMM or DIMM or chip or multichip or (multi near2 chip)) near2 module) cool\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 13:18
S62	1178	S61 ((heatsink or (heat or thermal or cool or cold or radiat\$3)) near3 (spread\$3 or plate or sink))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:18

S63	94	S62 ((spread\$3 or plate or sink) with (rotat\$4 or pivot or tilt))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:18
S64	4297	((heat or thermal\$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:53
S65	1020	S64 plug	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:58
S66	1	S65 ((PCB or board or substrate or carrier) with ((terminal or contact or plug) near2 strip))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:58
S67	167	S65 ((spread\$3 or plate or sink) with (IC or chip or semiconductor))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 13:59
S68	18	(gerold near2 gruendlner).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:11
S69	29	(juergen near2 hoegerl).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:11
S70	37	(volker near2 strutz).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:11
S71	19	(erich near2 syri).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:12
S72	60	(S68 or S69 or S70 or S71)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:12
S73	17212	infineon.as.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 14:14

S74	11	S73 ((heat or thermal \$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 14:14
S75	69	((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread\$3)) ((memory near2 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 14:18
S76	32	("3694699" "4528835" "4751146" "5365402" "5528456" "5567986" "5661339" "5673177" "5675474" "5719745" "5751553" "5757073" "5779134" "5830311"). PN. OR ("6031727"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2009/11/06 14:23
S77	143	((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread\$3)) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 14:29
S78	16	S77 ((spread\$3 or plate or sink) with (axis or rotat\$4 or pivot or tilt))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 14:30
S79	0	361/702.ccls. (((memory or IC or chip or semiconductor) near3 (module or strip)) (insert or plug) ((spread \$3 or plate or sink) with (axis or rotat\$4 or pivot or tilt)))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 15:25

S80	8	361/718-720.ccls. (((memory or IC or chip or semiconductor) near3 (module or strip)) (insert or plug) ((spread \$3 or plate or sink) with (axis or rotat\$4 or pivot or tilt)))	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 15:27
S81	0	"I3" ((heat or thermal \$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (rotat\$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/11/06 16:27
S82	9	((heat or thermal\$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (rotat \$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 16:29
S83	10	(((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread\$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	AND	ON	2009/11/06 16:30

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S84	9901	infineon.as.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 15:27
S85	9	(gerold near2 gruendlner).in.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 15:28
S86	10	(juergen near2 hoegerl).in.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 15:28

S87	21	(volker near2 strutz).in.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 15:28
S88	9	(erich near2 syri).in.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 15:28
S89	2095	361/719.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:16
S90	209	361/711.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:16
S91	524	361/702.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:16
S92	501	361/703.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:16
S93	1509	361/697.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17
S94	848	165/80.2.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17
S95	4168	165/80.3.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17
S96	3537	165/185.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17
S97	688	257/720.ccls.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17
S98	0	S84 (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:17

S99	0	(S85 or S86 or S87 or S88) (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:20
S100	0	"361"/\$.clm. (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:20
S101	5	"361"/\$.cls. (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:20
S102	1	"165"/\$.cls. (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:21

S103	9	"257"\$/ccls. (((cool or cold) near2 (plate)) or ((thermal or heat) near2 (sink or spread \$3))) (((IC or chip or semiconductor) near4 (strip or module or board or PCB)) with (plug or insert)) (PCB or board or substrate or carrier)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:21
S104	0	"I3" ((heat or thermal \$3) cool ((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SI MM)) (rotat\$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:28
S105	0	(S85 or S86 or S87 or S88) ((heat or thermal \$3) cool ((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SI MM)) (rotat\$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:28
S106	0	"361"\$/ccls. ((heat or thermal\$3) cool ((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SI MM)) (rotat \$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:28
S107	0	"165"\$/ccls. ((heat or thermal\$3) cool ((memory or DIMM or SI MM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SI MM)) (rotat \$4 or tilt or pivot\$4)).clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:28

S108	3	"257"/*.ccls. ((heat or thermal\$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (rotat \$4 or tilt or pivot\$4)). clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:28
S109	0	"165"/*.ccls. ((heat or thermal\$3) cool ((memory or DIMM or SIMM or (multi near2 chip) or multichip) near2 (strip or board or stick or module or DIMM or SIMM)) (rotat \$4 or tilt or pivot\$4)). clm.	US-PGPUB; USPAT; UPAD	AND	ON	2009/11/06 16:29

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